Roll No									Question Booklet Number	
O. M. R. Serial No.										

M. Sc. (Electronics) (Second Semester) EXAMINATION, July, 2022

ELECTRONIC CIRCUIT

Paper Code				
ELC	2	0	3	(N)

[Maximum Marks : 100

Questions Booklet Series

Time : 1:30 Hours]

Instructions to the Examinee:

- 1. Do not open the booklet unless you are asked to do so.
- 2. The booklet contains 60 questions. Examinee is required to answer any 50 questions in the OMR Answer-Sheet provided and not in the question booklet. If more than 50 questions are attempted by student, then the first attempted 50 questions will be considered for evaluation. All questions carry equal marks.
- 3. Examine the Booklet and the OMR Answer-Sheet very carefully before you proceed. Faulty question booklet due to missing or duplicate pages/questions or having any other discrepancy should be got immediately replaced.

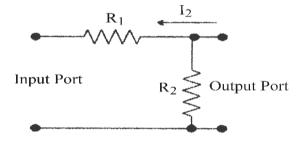
परीक्षार्थियों के लिए निर्देश :

- प्रश्न-पुस्तिका को तब तक न खोलें जब तक आपसे कहा न जाए।
- 2. प्रश्न-पुस्तिका में 60 प्रश्न हैं। परीक्षार्थी को किन्हीं 50 प्रश्नों को केवल दी गई OMR आन्सर-शीट पर ही हल करना है, प्रश्न-पुस्तिका पर नहीं। यदि छात्र द्वारा 50 से अधिक प्रश्नों को हल किया जाता है तो प्रारम्भिक हल किये हुए 50 उत्तरों को ही मूल्यांकन हेतु सम्मिलित किया जाएगा। सभी प्रश्नों के अंक समान हैं।
- उत्तर अंकित करने से पूर्व प्रश्न-पुस्तिका तथा OMR आन्सर-शीट को सावधानीपूर्वक देख लें। दोषपूर्ण प्रश्न-पुस्तिका जिसमें कुछ भाग छपने से छूट गए हों या प्रश्न एक से अधिक बार छप गए हों या उसमें किसी अन्य प्रकार की कमी हो, तो उसे तुरन्त बदल लें।

(शेष निर्देश अन्तिम पृष्ठ पर)

(Only for Rough Work)

- 1. In a hybrid model of a two-port network, parameter h_{11} is also known as :
 - (A) Input conductance
 - (B) Input resistance
 - (C) Output conductance
 - (D) Output resistance
- 2. For the two-port network shown below, assume $R_1 = R_2 = 10 \text{ k}\Omega$. What is the value of the hybrid parameter h_{22} ?



- (A) 0.1 mho
- (B) 10 mhos
- (C) 20 mhos
- (D) 15 mhos
- 3. What are the hybrid parameters used to analyze?
 - (A) MOSFET
 - (B) Junction Field Effect Transistor
 - (C) Bipolar Junction Transistor
 - (D) It has no use.

- 4. What type of amplifier is an emitter follower amplifier ?
 - (A) Voltage amplifier
 - (B) Wideband amplifier
 - (C) Feedback amplifier
 - (D) Power amplifier
- 5. How is the output impedance of an emitter follower amplifier?
 - (A) Irrelevant
 - (B) Moderate
 - (C) Low
 - (D) High
- 6. What type of negative feedback does the emitter follower amplifier provide ?
 - (A) Voltage, current and power
 - (B) Voltage
 - (C) Current
 - (D) Power
- 7. What is the voltage gain of an emitter follower amplifier?
 - (A) 1
 - (B) β
 - (C) ∞
 - (D) 0

- 8. What is the other name for an emitter follower amplifier configuration ?
 - (A) Common collector
 - (B) Common base
 - (C) Common emitter
 - (D) Amplifier circuit
- 9. What is the application of a high input resistance transistor amplifier circuit amplifier?
 - (A) Positive feedback
 - (B) Voltage gain
 - (C) Power gain
 - (D) Impedance matching
- 10. How is the power gain of a high input resistance transistor amplifier and circuit amplifier?
 - (A) Irrelevant
 - (B) Moderate
 - (C) Low
 - (D) High

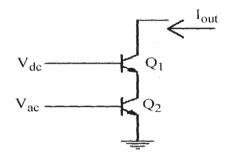
- 11. In an RC coupled CE amplifier, when the input frequency increases, which of these are incorrect?
 - (A) Reactance C_{SH} decreases.
 - (B) Voltage gain increases.
 - (C) Voltage gain decreases due to shunt capacitance.
 - (D) An RC coupled amplifier behaves like a low pass filter.
- 12. Which of the statement is incorrect?
 - (A) At unity gain frequency the CE short circuit current gain becomes 1.
 - (B) Unity gain frequency is the same asGain Bandwidth Product ofBJT.
 - (C) Gain of BJT decreases at higher frequencies due to junction capacitances.
 - (D) β -cut-off frequency is one where the CE short circuit current gain becomes $\beta/2$.

ELC-203(N) (4) Set-C

- 13. What is the condition to achieve oscillations?
 - $(A) \quad |AB| = 1$
 - (B) $\angle AB = 0^{\circ}$
 - (C) $\angle AB = \text{ multiples of } 2\pi$
 - (D) All the mentioned
- 14. Given a MOSFET where gate to source capacitance is 300 pF and gate to drain capacitance is 500 pF. Calculate the gain bandwidth product if the transconductance is 30 m Ω^{-1} .
 - (A) 5.98 MHz
 - (B) 4.9 MHz
 - (C) 6.5 MHz
 - (D) 5.22 MHz
- 15. The lower and upper cutoff frequencies of an amplifier are unknown. If originally, individual BW of such an amplifier is B_1 , and now the bandwidth of the cascaded network of 10 such amplifiers is B_2 , find B_2/B_1 .
 - (A) 0.26
 - (B) 3.84
 - (C) Insufficient data
 - (D) 5

- 16. For any cascaded amplifier network, which of these are incorrect?
 - (A) Cascading increases gain.
 - (B) Overall input resistance is equal to the input resistance of the first amplifier.
 - (C) The overall output resistance is less than the lowest output resistance in all amplifiers used.
 - (D) Loading effect occurs.
- 17. Which of these is incorrect about Darlington amplifier?
 - (A) It has a high input resistance.
 - (B) The output resistance is low.
 - (C) It has a unity voltage gain.
 - (D) It is a current buffer.
- 18. What is a cascade amplifier?
 - (A) A cascade of two CE amplifiers
 - (B) A cascade of two CB amplifiers
 - (C) A cascade of CE and CB amplifiers
 - (D) A cascade of CB and CC amplifiers

19. Consider the figure shown:



Given that $g_{m_1} = 30 \text{ m}\Omega^{-1}$ and $g_{m_2} = 50 \text{ m}\Omega^{-1}$, $\alpha_1 = 1.1$, $\alpha_2 = 1.5$.

What is the transconductance of the entire network?

- (A) $80 \text{ m} \Omega^{-1}$
- (B) $75 \text{ m} \Omega^{-1}$
- (C) $33 \text{ m}\Omega^{-1}$
- (D) $55 \text{ m}\Omega^{-1}$
- 20. Which of the following is not an LC oscillator?
 - (A) Hartley oscillator
 - (B) Colpitts oscillator
 - (C) Crystal oscillator
 - (D) Clapp oscillator
- 21. Given that a feedback network is shuntseries, and output load is $10 \text{ k}\Omega$. What is the output voltage across it given that transfer gain is 10, source current is 20 mA and feedback current is 10 mA?
 - (A) 1 V
 - (B) 2 V
 - (C) 10 V
 - (D) 20 V

- 22. Wide band amplifiers are most commonly
 - (A) Single ended
 - (B) Double ended
 - (C) Unpredictable
 - (D) None of the mentioned
- 23. The gain of an amplifier with feedback is known as gain.
 - (A) Resonant
 - (B) Open loop
 - (C) Closed loop
 - (D) None of the above
- 24. A negative-feedback amplifier is an amplifier.
 - (A) Magnetic
 - (B) Electronic
 - (C) Electromagnetic
 - (D) None of the above
- - (A) High
 - (B) Very high
 - (C) Almost zero
 - (D) Low
- - (A) 500
 - (B) 100
 - (C) 1000
 - (D) 5000

27.	When a negative voltage feedback	31.	In an unregulated power supply, if		
	is applied to an amplifier, its		load current increases, the output		
	bandwidth	voltage			
	(A) is increased		(A) Remains the same		
	(B) insufficient data		(B) Decreases		
	(C) is decreased		(C) Increases		
	(D) remains the same		(D) None of the above		
28.	When current feedback (negative) is	32.	A power supply which has voltage		
	applied to an amplifiers, its input		regulation of is unregulated		
	impedance		power supply.		
	(A) remains the same		(A) 0%		
	(B) is decreased		(B) 5%		
	(C) is increased		(C) 10%		
	(D) None of the above		(D) 8%		
29.	The value of negative feedback fraction is	33.	A Zener diode utilises		
	always	55.	characteristic for voltage regulation.		
	(A) Less than 1				
	(B) More than 1		(A) Forward		
	(C) Equal to 1		(B) Reverse		
	(D) None of the above		(C) Both forward and reverse		
	(=) - 1.1111 01 1111 110 1.11		(D) None of the above		
30.	A feedback circuit usually employs	34.	A Zener diode is used as a		
	network.		voltage regulating device.		
	(A) Resistive		(A) Shunt		
	(B) Capacitive		(B) Series		
	(C) Inductive		(C) Series-shunt		
	(D) None of the above		(D) None of the above		

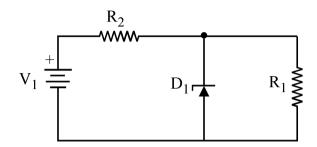
35.	As the junction temperature increases, the	38.	Zener diode is generally made
	voltage breakdown point for Zener		of
	mechanism		(A) Germanium
	(A) is increased		(B) Silicon
	(B) is decreased		(C) Carbon
	(C) remains the same		(D) None of the above
	(D) None of the above		
		39.	A Zener diode
36.	In a 15 V Zener diode, the breakdown		(A) is a battery
	mechanism will occur by		(D) and like a bottom in the breekdown
(A) Avalanche mechanism(B) Zener mechanism	(A) Avalanche mechanism		(B) acts like a battery in the breakdown
	(P) Zanar maahaniam		region
	(b) Zeilei mechanism		(C) has a barrier potential of 1 V
	(C) Both Zener and avalanche		
	mechanism		(D) is forward biased
	(D) None of the above	40.	Two similar 15 V Zeners are connected in
37.	Another name for Zener diode is		series. What is the regulated output
	diode.		voltage?
	diode.		
	(A) Breakdown		(A) 15 V
	(B) Voltage		(B) 5 V
	(C) Power		(C) 30 V
	(D) Current		(D) 45 V

(8)

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ELC-203(N)

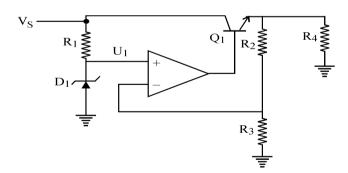
41. Consider the circuit shown below where the breakdown voltage of the diode is5 V. Source voltage varies between 6 V to 12 V.



Find the maximum current through the $R_2,$ given that $R_1=2~k\Omega$ and $R_2=5~k\Omega\,.$

- (A) 3.5 mA
- (B) 1 mA
- (C) 1.4 mA
- (D) 0.2 mA
- 42. are the type of linear voltage regulators.
 - (A) Series
 - (B) Shunt
 - (C) Both (A) and (B)
 - (D) None of the above

- 43. Which one is a type of switching voltage regulator?
 - (A) Step-up
 - (B) Step-down
 - (C) Inverter
 - (D) All of the above
- 44. What is not related to a transistorized series regulator?
 - (A) The output can be varied by using a variable resistor.
 - (B) The output is independent of temperature.
 - (C) The overload and short circuit protection is not required.
 - (D) The circuit has negative feedback responsible for regulation.
- 45. Consider the Op-amp circuit shown.



The breakdown voltage of the Zener is 5 V. β for the transistor is 100. $R_1=10~k\Omega$, $R_2=90~k\Omega$, $R_3=30~k\Omega$, $R_4=50~k\Omega$. Calculate the total output voltage.

- (A) 20 V
- (B) 30 V
- (C) 5 V
- (D) 50 V

46. Reverse saturation current of a common 49. Which of the following statements about emitter transistor is a common emitter transistor is true? Collector current when emitter is Very high input resistance (A) open circuited and base-collector High output resistance (B) junction is reverse biased (C) Current gain is less than unity (B) Emitter current when collector is open circuited and base-collector Voltage gain is very low (D) junction is reverse biased 50. Which of the following configurations is Base current when emitter circuit is (C) used for impedance matching? open circuited and emitter-collector junction is reverse biased Common base configuration (D) Collector current when base circuit Common emitter configuration (B) is open circuited Common collector configuration (C) (D) All configurations are equally 47. Reverse collector saturation current I_{CBO} suited. is What is Barkhausen 51. criterion for (A) Collector current when emitter oscillation? current is zero Collector current when base current AB > 1is zero AB < 1(B) Same as reverse saturation current (C) AB = 1(C) (D) Collector current when either $AB \neq 1$ (D) emitter or base current is zero 48. Which of the following statements about An oscillator is a type of: 52. a common base transistor is true? Feedforward amplifier (A) (A) Very low input impedance (B) Feedback amplifier (B) Very low output impedance Waveform amplifier (C) (C) Current gain is greater than unity

Voltage gain is very low

(D)

(D)

RC amplifier

53.	What happens if $ AB < 1$?	57.	The dimensions of h_{je} parameters
	(A) Oscillation will die down		are
	(B) Oscillation will keep on increasing		(A) MHO
	(C) Oscillation remains constant		(B) OHM
	(D) Oscillation fluctuates		(C) Farad
	(D) Osemanon mactanes		(D) Ampere
54.	During high frequency applications of a		
	B. J. T., which parasitic capacitors arise	58.	The h_{fe} parameter is called in CE arrangement with output short
	between the base and the emitter?		CE arrangement with output short circuited.
	(A) C_{je} and C_b		(A) Voltage gain
	(B) C_{cs}		(B) Current gain
	(C) C_b		(C) Input impedance
	(D) C_{cs} and C_b		(D) Output impedance
55.	The <i>h</i> -parameters analysis gives correct	59.	If temperature changes, h parameters of a
			transistor
	results for		(A) also change
	(A) large signals only		(B) do not change
	(B) small signals only		(C) remain same
	(C) both large and small		(D) may or may not change
	(D) not large nor small signals	60.	The values of <i>h</i> -parameters of a transistor
56.			in CE arrangement are
	How many h -parameters are there for a		arrangement.
	transistor ?		_
	(A) Two		(A) same as for CB
	(A) Two		(B) same as for CC
	(B) Three		(C) different from that in CB
	(C) Four		(D) similar to no
	(D) Five		

(11)

Set-C

ELC-203(N)

4. Four alternative answers are mentioned for each question as—A, B, C & D in the booklet. The candidate has to choose the most correct/appropriate answer and mark the same in the OMR Answer-Sheet as per the direction:

Example:

Question:

Q.1 (A) (C) (D)
Q.2 (A) (B) (C) (D)
Q.3 (A) (C) (D)

Illegible answers with cutting and over-writing or half filled circle will be cancelled.

- 5. Each question carries equal marks. Marks will be awarded according to the number of correct answers you have.
- 6. All answers are to be given on OMR Answer sheet only. Answers given anywhere other than the place specified in the answer sheet will not be considered valid.
- 7. Before writing anything on the OMR Answer Sheet, all the instructions given in it should be read carefully.
- 8. After the completion of the examination candidates should leave the examination hall only after providing their OMR Answer Sheet to the invigilator. Candidate can carry their Question Booklet.
- 9. There will be no negative marking.
- 10. Rough work, if any, should be done on the blank pages provided for the purpose in the booklet.
- 11. To bring and use of log-book, calculator, pager and cellular phone in examination hall is prohibited.
- 12. In case of any difference found in English and Hindi version of the question, the English version of the question will be held authentic.
- Impt.: On opening the question booklet, first check that all the pages of the question booklet are printed properly. If there is ny discrepancy in the question Booklet, then after showing it to the invigilator, get another question Booklet of the same series.

4. प्रश्न-पुस्तिका में प्रत्येक प्रश्न के चार सम्भावित उत्तर—
A, B, C एवं D हैं। परीक्षार्थी को उन चारों विकल्पों में से
एक सबसे सही अथवा सबसे उपयुक्त उत्तर छाँटना है।
उत्तर को OMR आन्सर-शीट में सम्बन्धित प्रश्न संख्या में
निम्न प्रकार भरना है:

उदाहरण :

प्रश्न :

प्रश्न 1 (A) (C) (D) प्रश्न 2 (A) (B) (D) प्रश्न 3 (A) (C) (D)

अपठनीय उत्तर या ऐसे उत्तर जिन्हें काटा या बदला गया है, या गोले में आधा भरकर दिया गया, उन्हें निरस्त कर दिया जाएगा।

- 5. प्रत्येक प्रश्न के अंक समान हैं। आपके जितने उत्तर सही होंगे, उन्हीं के अनुसार अंक प्रदान किये जायेंगे।
- 6. सभी उत्तर केवल ओ. एम. आर. उत्तर-पत्रक (OMR Answer Sheet) पर ही दिये जाने हैं। उत्तर-पत्रक में निर्धारित स्थान के अलावा अन्यत्र कहीं पर दिया गया उत्तर मान्य नहीं होगा।
- 7. ओ. एम. आर. उत्तर-पत्रक (OMR Answer Sheet) पर कुछ भी लिखने से पूर्व उसमें दिये गये सभी अनुदेशों को सावधानीपूर्वक पढ लिया जाये।
- 8. परीक्षा समाप्ति के उपरान्त परीक्षार्थी कक्ष निरीक्षक को अपनी OMR Answer Sheet उपलब्ध कराने के बाद ही परीक्षा कक्ष से प्रस्थान करें। परीक्षार्थी अपने साथ प्रश्न-पुस्तिका ले जा सकते हैं।
- 9. निगेटिव मार्किंग नहीं है।
- 10. कोई भी रफ कार्य, प्रश्न-पुस्तिका के अन्त में, रफ-कार्य के लिए दिए खाली पेज पर ही किया जाना चाहिए।
- 11. परीक्षा-कक्ष में लॉग-बुक, कैलकुलेटर, पेजर तथा सेल्युलर फोन ले जाना तथा उसका उपयोग करना वर्जित है।
- 12. प्रश्न के हिन्दी एवं अंग्रेजी रूपान्तरण में भिन्नता होने की दशा में प्रश्न का अंग्रेजी रूपान्तरण ही मान्य होगा।

महत्वपूर्ण : प्रश्नपुस्तिका खोलने पर प्रथमतः जाँच कर देख लें कि प्रश्न-पुस्तिका के सभी पृष्ठ भलीभाँति छपे हुए हैं। यदि प्रश्नपुस्तिका में कोई कमी हो, तो कक्षनिरीक्षक को दिखाकर उसी सिरीज की दूसरी प्रश्न-पुस्तिका प्राप्त कर लें।